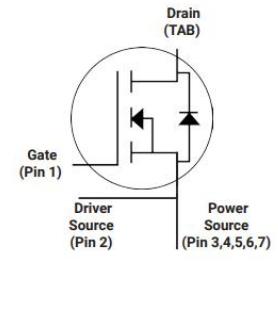


TriQSiC™1200V Silicon Carbide Power MOSFET G1

(N Channel Enhancement)

Features

- High speed switching
- Very low switching losses
- IGBT-compatible driving voltage (15V for turn-on)
- Fully controllable dv/dt
- High blocking voltage with low on-resistance
- Fast intrinsic diode with low reverse recovery (Qrr)
- Temperature independent turn-off switching losses
- Halogen free, RoHS compliant
- Automotive Qualified (AEC-Q101)



Benefits

- Cooling effort reduction
- Efficiency improvement
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency



Applications

- On-board charger/PFC
- EV battery chargers
- Booster/DC-DC converter
- Switch mode power supplies

Table 1 Key performance and package parameters

Type	V _{DS}	I _{DS} (T _C = 25°C, R _{th(j-c),max})	R _{Ds(on), typ} (V _{GS} = 15V, I _D = 20A, T _J = 25°C)	T _{J,max}	Marking	Package
S1M075120J2	1200 V	39 A	75 mΩ	175 °C	S1M075120J2	TO263-7L

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1、Maximum ratings

Table 2 Maximum rating (T_c = 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V _{DS,max}	Drain source voltage	1200	V	V _{GS} = 0V, I _D = 100μA	
V _{GS,max}	Gate source voltage	-10 /+22	V	Absolute maximum values	Note1
V _{GSpulse}	Gate-source voltage,max. transient voltage	-10 /+25	V	t _p ≤0.5μs, D<0.01	
V _{GSop}	Gate source voltage	-4 /+15~18	V	Recommended operational values	
I _D	Continuous drain current	39	A	V _{GS} = 15V, T _C = 25°C	Fig.19
		28		V _{GS} = 15V, T _C = 100°C	
I _{D(pulse)}	Pulsed drain current	87	A	Pulse width t _p limited by T _{J,max}	Fig.22
P _D	Power dissipation	169	W	T _C = 25°C, T _J = 175°C	Fig.20
T _J , T _{stg}	Operating Junction and storage temperature	-55 to +175	°C		
T _L	Soldering temperature	260	°C	1.6mm (0.063") from case for 10s	

Note 1: when using MOSFET Body Diode V_{GS,max} = -4 / +22V

2、Thermal characteristics

Table 3 Thermal characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
R _{th(j-c)}	Thermal resistance from junction to case	-	0.89	1.11	°C/W	-	Fig.21

3、Electrical characteristics

3.1 Static characteristics

Table 4 Static characteristics (T_c = 25°C unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
V _{(BR)DSS}	Drain-source breakdown voltage	1200	-	-	V	V _{GS} = 0V, I _D = 100μA	
V _{GS(th)}	Gate threshold voltage	2.3	2.8	3.6	V	V _{DS} = V _{GS} , I _D = 5mA	Fig.11
		-	2.1	-	V	V _{DS} = V _{GS} , I _D = 5mA, T _J = 175°C	
I _{DSS}	Zero gate voltage drain current	-	1	10	μA	V _{DS} = 1200V, V _{GS} = 0V	
I _{GSS}	Gate source leakage current	-	-	100	nA	V _{GS} = 15V, V _{DS} = 0V	
R _{DS(on)}	Current drain-source on-state resistance	-	75	98	mΩ	V _{GS} = 15V, I _D = 20A	Fig.4,5,6
		-	110	-		V _{GS} = 15V, I _D = 20A, T _J = 175°C	
		-	60	78		V _{GS} = 18V, I _D = 20A	
		-	109	-		V _{GS} = 18V, I _D = 20A, T _J = 175°C	
g _{fs}	Transconductance	-	12	-	S	V _{DS} = 20V, I _D = 20A	Fig.7
		-	11	-		V _{DS} = 20V, I _D = 20A, T _J = 175°C	
R _{g,int}	Internal gate resistance	-	1.5	-	Ω	V _{AC} = 25mV, f = 1MHz, open drain	

3.2 Dynamic characteristics

Table 5 Dynamic characteristics (T_c = 25°C unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
C _{iss}	Input capacitance	-	1037	-	pF	V _{DS} = 1000V, V _{GS} = 0V T _J = 25°C, V _{AC} = 25mV f = 1MHz	Fig.17,18
C _{oss}	Output capacitance	-	65	-			
C _{rss}	Reverse capacitance	-	3.8	-			
E _{oss}	Coss stored energy	-	40	-	μJ		

1200V SiC Power MOSFET

Q_{gs}	Gate source charge	-	13	-	nC	$V_{DS} = 800V$, $V_{GS} = -4/+15V$ $I_D = 20A$	Fig.12
Q_{gd}	Gate drain charge	-	19	-			
Q_g	Gate charge	-	45	-			

3.3 Switching characteristics

Table 6 Dynamic characteristics ($T_c = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
E_{on}	Turn on switching energy	-	110	-	μJ	$V_{DS} = 800V$, $V_{GS} = -4/+15V$ $I_D = 20A$, $R_g = 0\Omega$ $L = 120\mu H$	Fig.26
E_{off}	Turn off switching energy	-	23	-			
$t_{d(on)}$	Turn on delay time	-	9.6	-	ns	$V_{DS} = 800V$, $V_{GS} = -4/+15V$ $I_D = 20A$, $R_g = 0\Omega$ $L = 120\mu H$	Fig.27
t_r	Rise time	-	7.8	-			
$t_{d(off)}$	Turn off delay time	-	14.8	-			
t_f	Fall time	-	7.5	-			

Table 7 Body diode characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
V_{SD}	Diode forward voltage	-	3.7	-	V	$V_{GS} = -4V$, $I_{SD} = 10A$	Fig.8,9, 10
		-	3.4	-	V	$V_{GS} = -4V$, $I_{SD} = 10A$ $T_J = 175^\circ C$	
I_S	Continuous diode forward current	-	36	-	A	$V_{GS} = -4V$, $T_c = 25^\circ C$	Note2
t_{rr}	Reverse recovery time	-	57	-	ns	$V_R = 800V$, $V_{GS} = -4V$ $I_{SD} = 20A$ $di/dt = 1100A/\mu s$ $T_J = 175^\circ C$	
Q_{rr}	Reverse recovery charge	-	162	-	nC		
I_{rrm}	Peak reverse recovery current	-	5.9	-	A		

Note 2: When using SiC Body Diode the maximum recommended $V_{GS} = -4 V$

4. Electrical characteristic diagrams

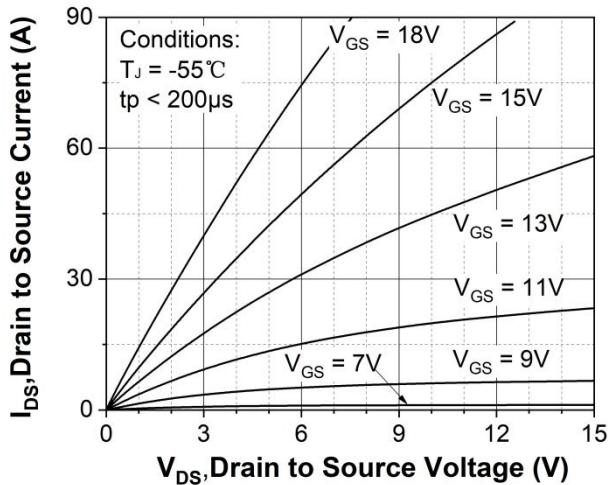


Figure 1. Output characteristics $T_J = -55^\circ\text{C}$

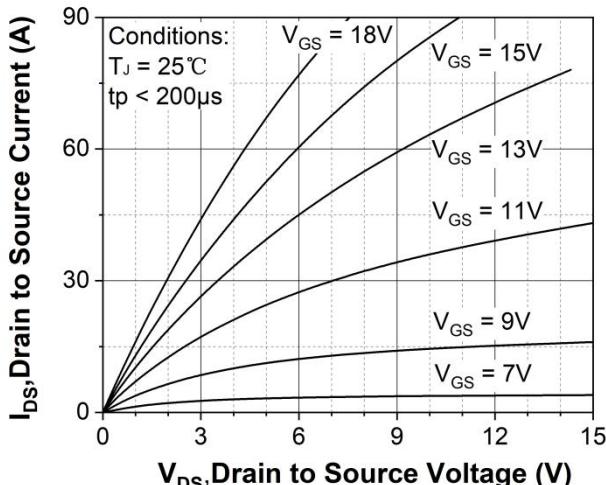


Figure 2. Output characteristics $T_J = 25^\circ\text{C}$

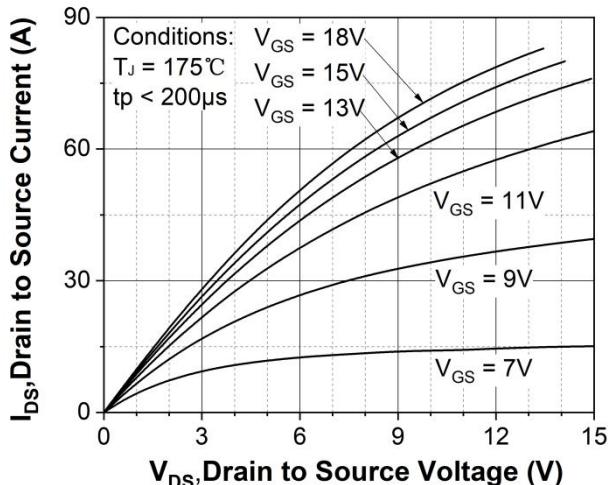


Figure 3. Output characteristics $T_J = 175^\circ\text{C}$

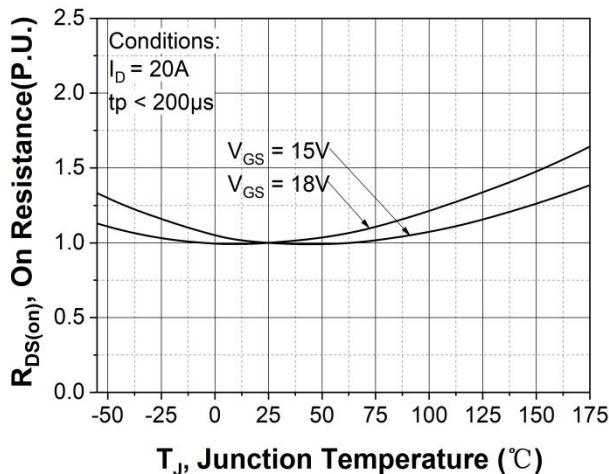


Figure 4. Normalized on-resistance vs. temperature

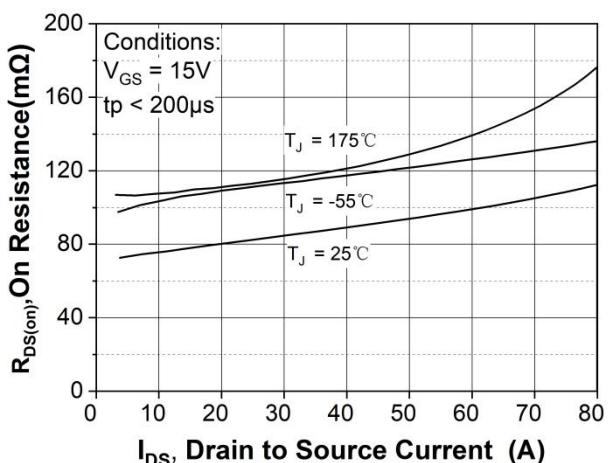


Figure 5. On-resistance vs. drain current

Datasheet

for various temperatures

清纯半导体（宁波）有限公司

for various gate voltages

V03_00

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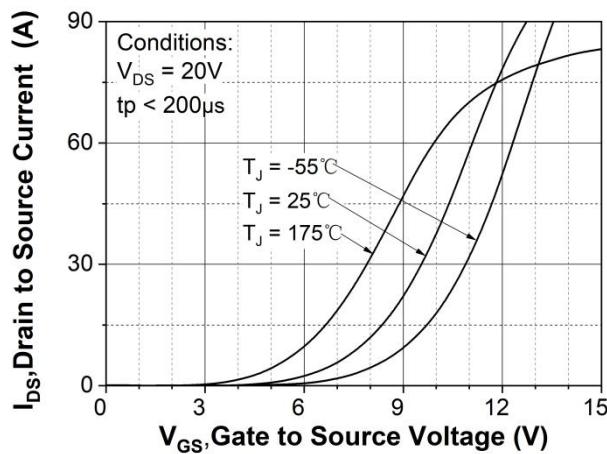


Figure 7. Transfer characteristic for various junction temperatures

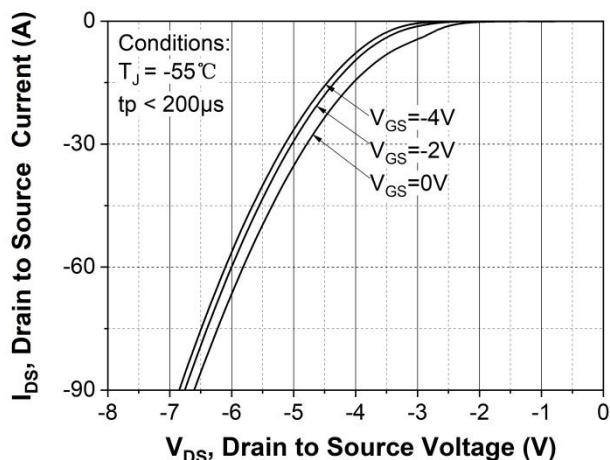


Figure 8. Body diode characteristic at $T_J = -55^{\circ}C$

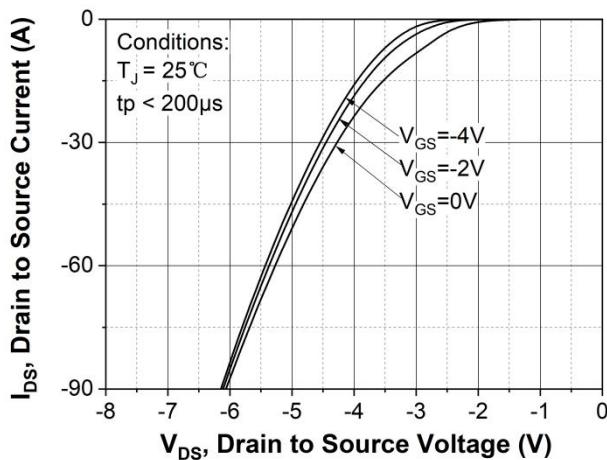


Figure 9. Body diode characteristic at $T_J = 25^{\circ}C$

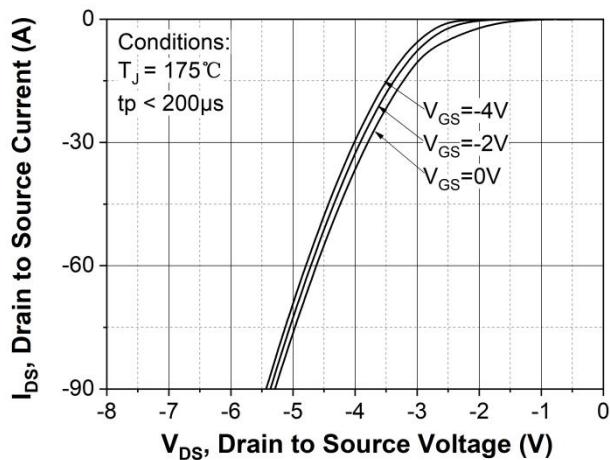


Figure 10. Body diode characteristic at $T_J = 175^{\circ}C$

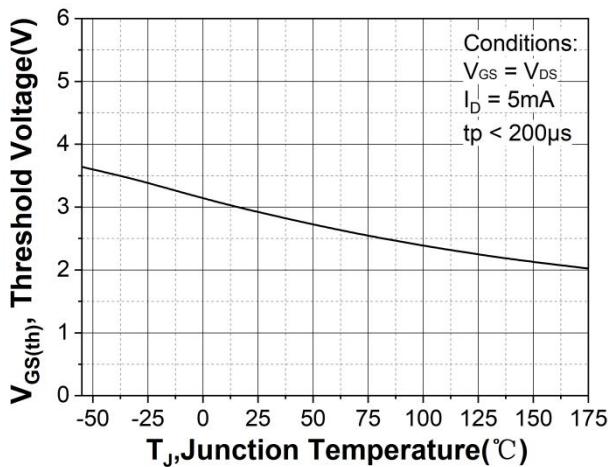


Figure 11. Threshold voltage vs. temperature

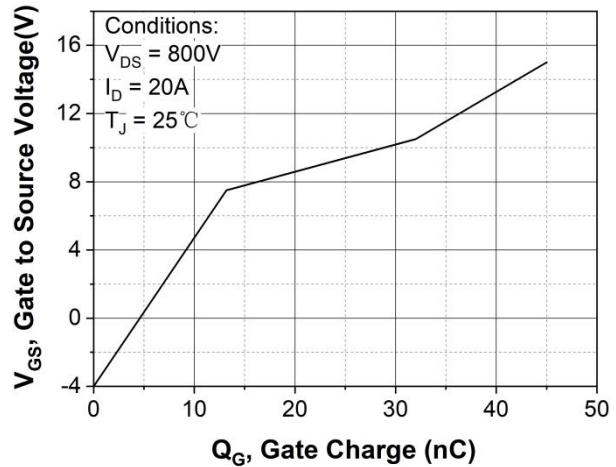


Figure 12. Gate charge characteristic

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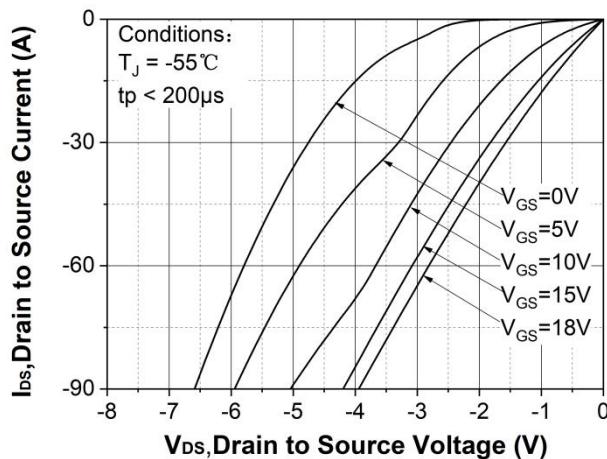


Figure 13. 3rd quadrant characteristic at $T_J = -55^\circ\text{C}$

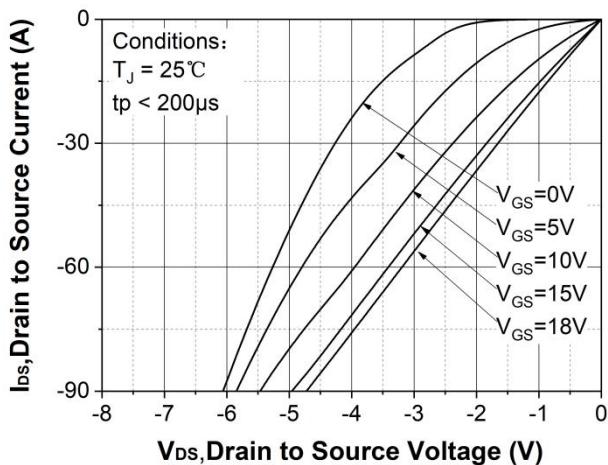


Figure 14. 3rd quadrant characteristic at $T_J = 25^\circ\text{C}$

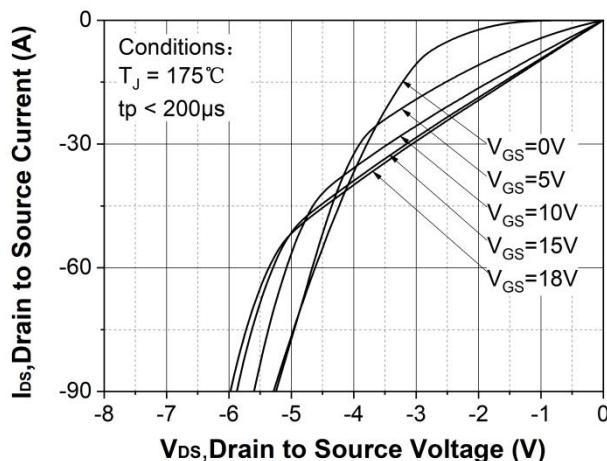


Figure 15. 3rd quadrant characteristic at $T_J = 175^\circ\text{C}$

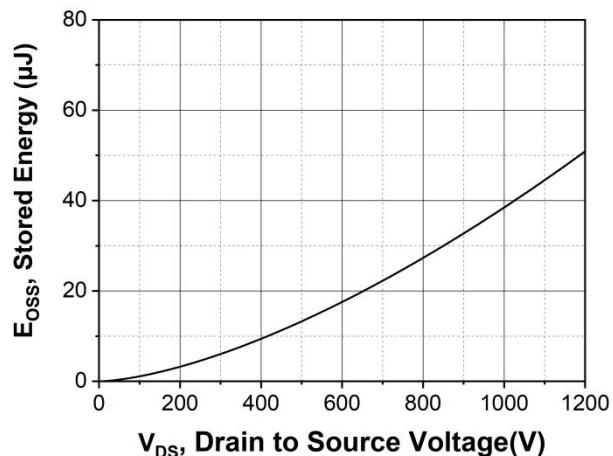


Figure 16. Output capacitor stored energy

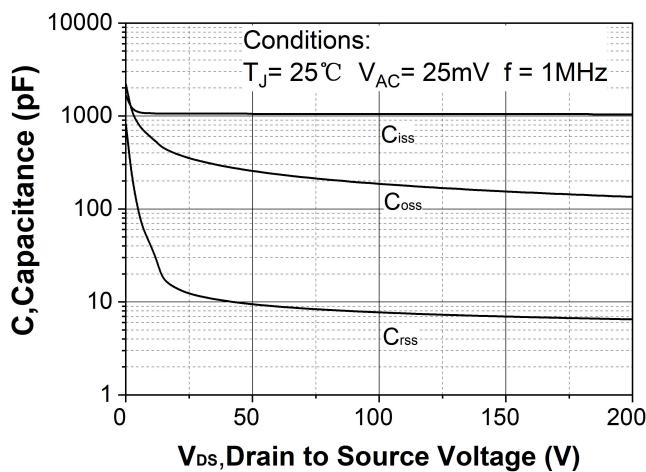


Figure 17. Capacitances vs. drain-source voltage (0 - 200V)

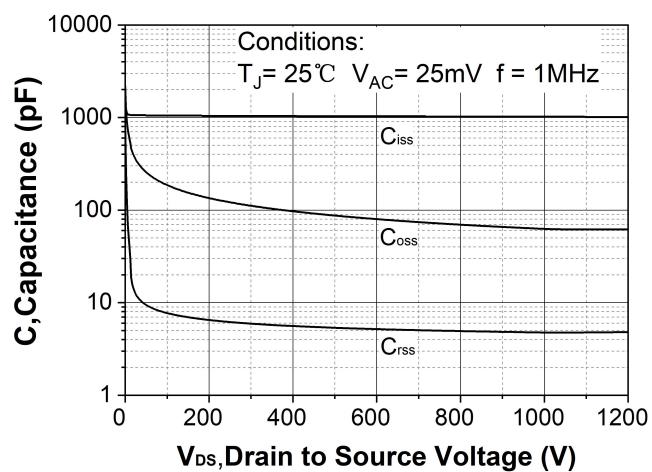
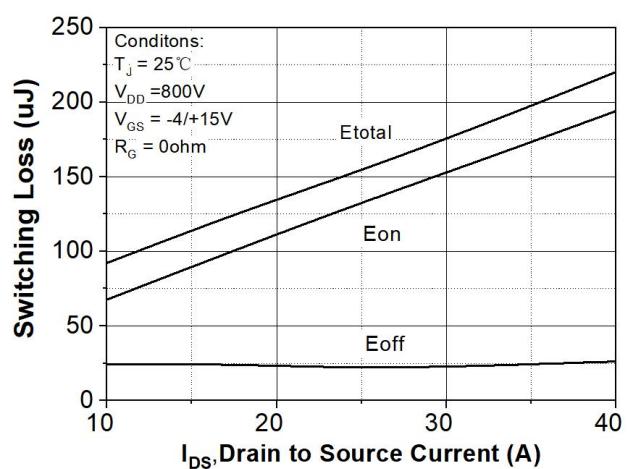
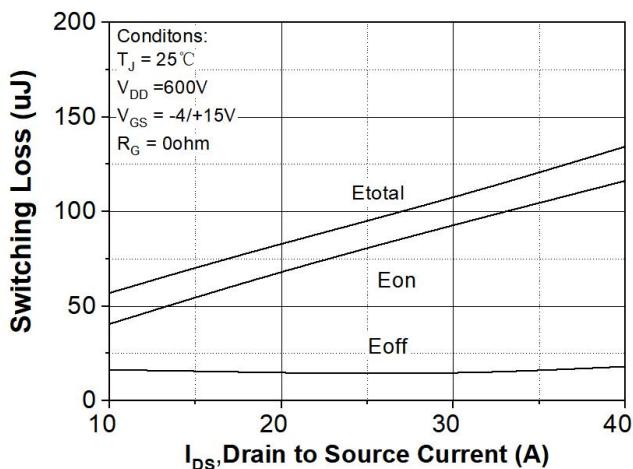
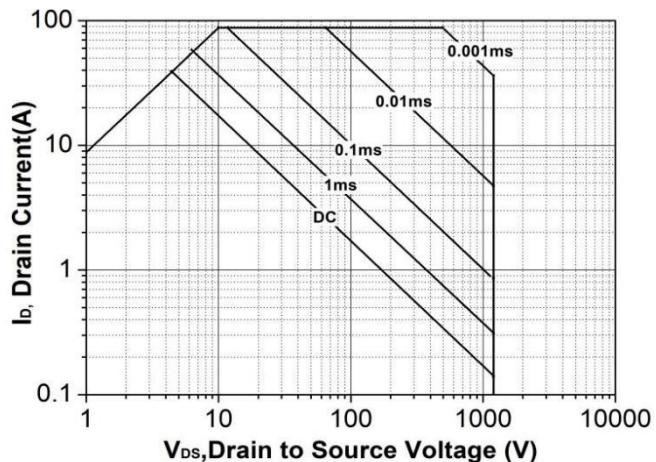
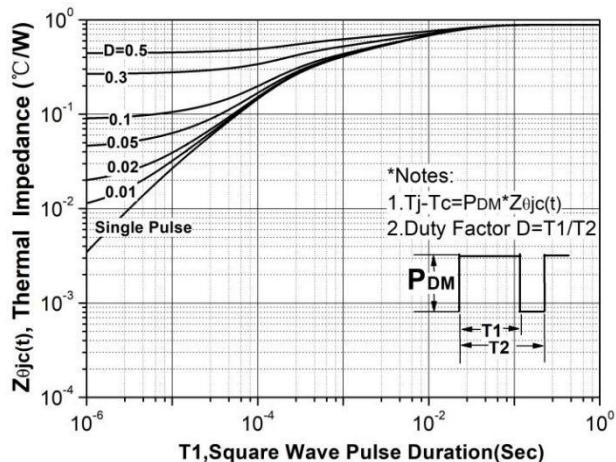
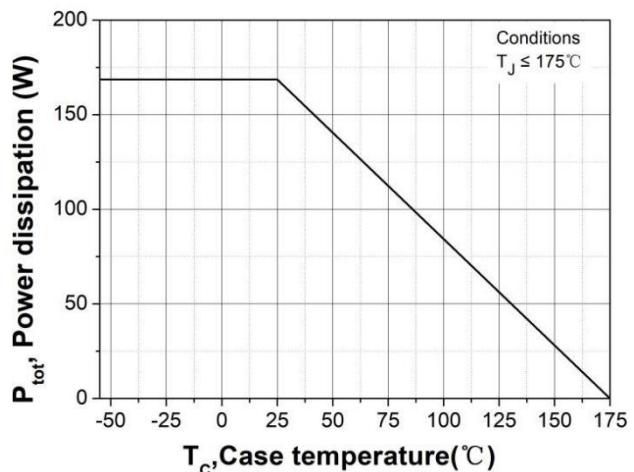
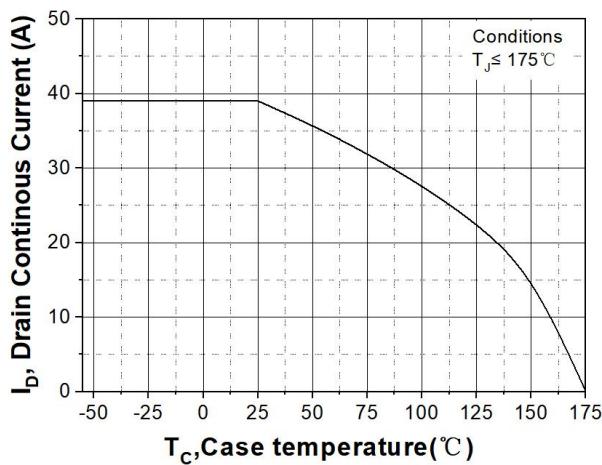


Figure 18. Capacitances vs. drain-source voltage (0 - 1200V)



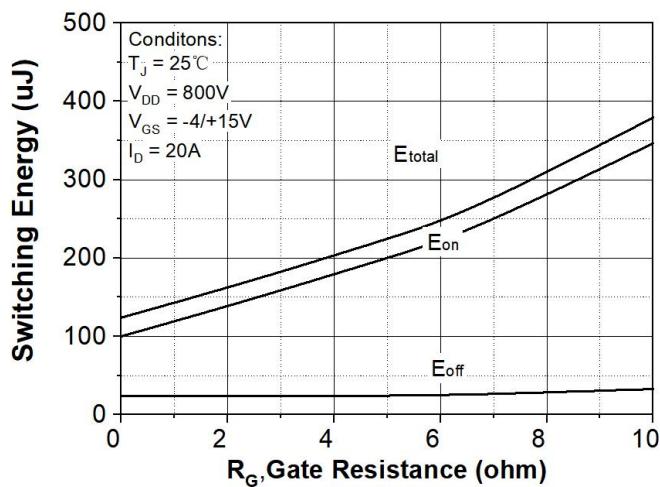


Figure 25. Clamped inductive switching energy vs. R_G (ext)

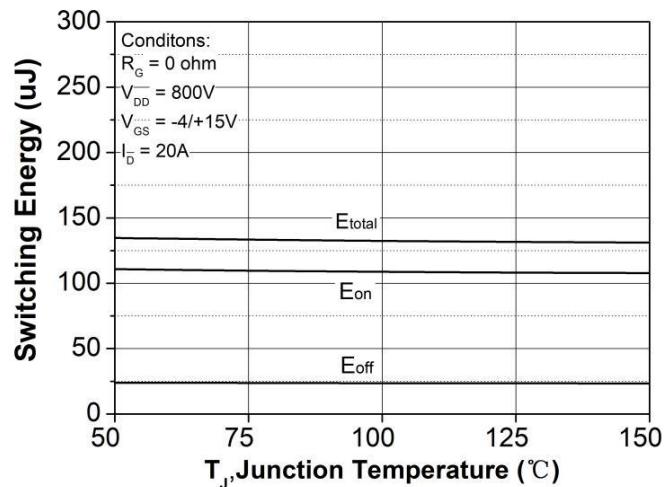


Figure 26. Reverse characteristics vs. T_J

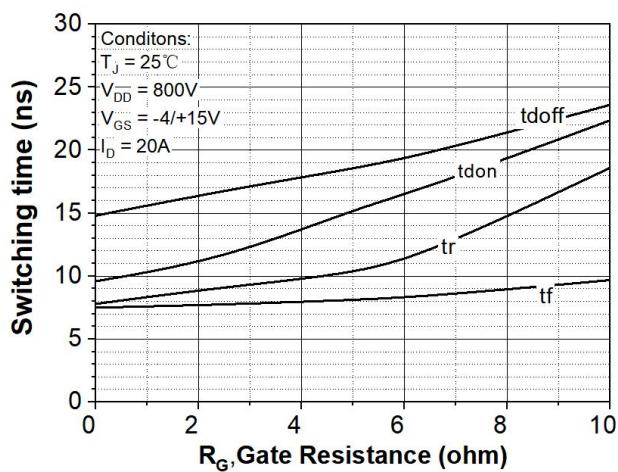
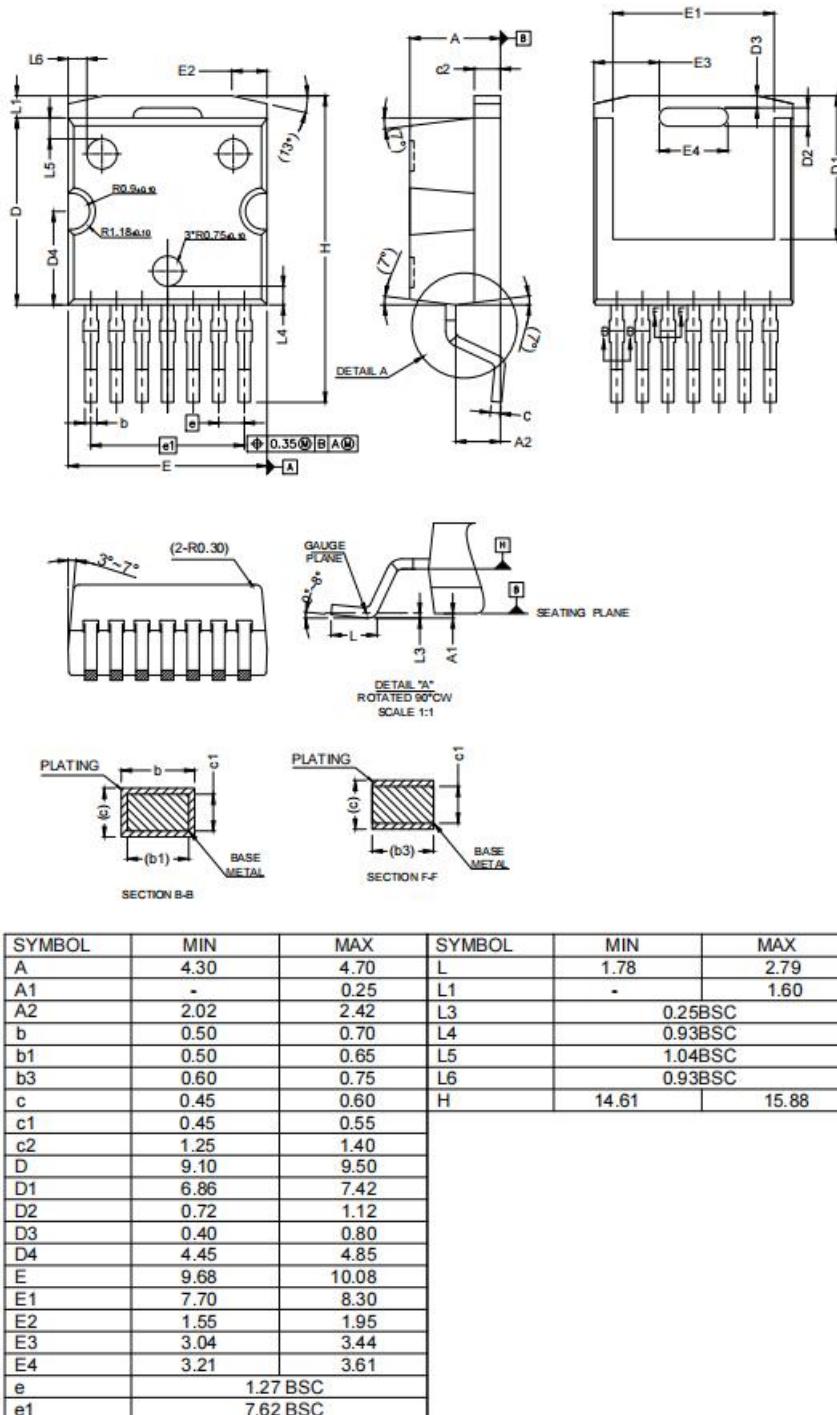


Figure 27. Switching times vs. R_G (ext)

5、Package drawing (TO-263-7L-A)



6、Test conditions

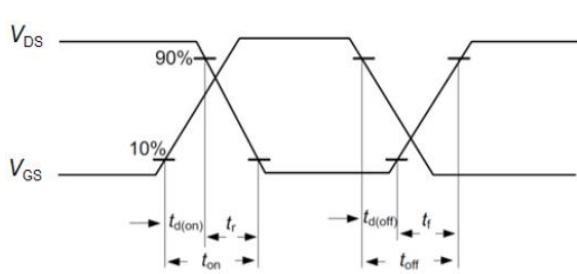


Figure A. Definition of switching times

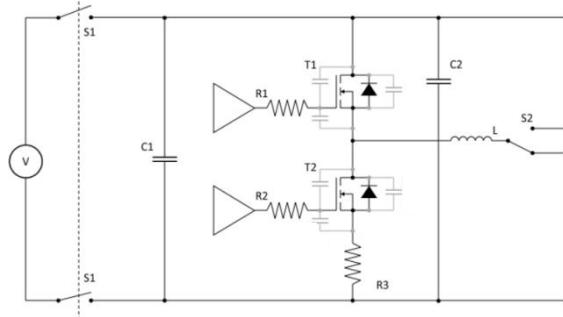


Figure B. Dynamic test circuit

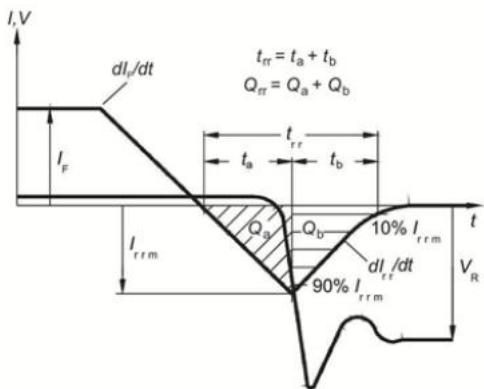


Figure C. Definition of body diode
switching characteristics

Revision history

Document version	Date of release	Description of changes
V01_00	2022-12-30	---
V02_00	2023-09-15	---
V02_01	2025-03-28	---
V03_00	2025-04-09	---

Attention

1. RoHS compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/ EC (RoHS2), as implemented January 2, 2013.

2. REACH compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Sichain representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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