

S1P04R120HBH-B (Preliminary)



1200V / 400A All-Silicon Carbide MOSFET Half-Bridge Module

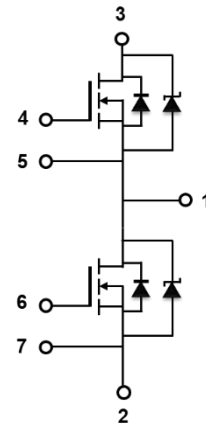
Features

- Electrical features
 - $V_{DSS} = 1200V$
 - $I_{D, nom} = 400A$
- High-speed Switching Possible
- High Power Density
- High Frequency Operation
- Ultra-low Losses



Applications

- DC/DC converters
- UPS system
- High power converters
- Photovoltaics, wind power generation
- Induction heating equipment
- Electrified vehicle traction inverter



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1、Maximum ratings

Table 1 Maximum rating (Tc = 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V _{DS,max}	Drain source voltage	1200	V	V _{GS} = 0V, I _D = 100 μA	
V _{GS,max}	Gate source voltage	-8 /+22	V	Absolute maximum values	
V _{GSop}	Gate source voltage	-4 /+18	V	Recommended operational values	
I _D	Continuous drain current	400	A	V _{GS} = 18V, T _C = 100°C	
I _{D(pulse)}	Pulsed drain current	800	A	Pulse width tp limited by T _{j,max}	
T _J ,T _{stg}	Operating Junction and storage temperature	-40 to +150	°C		

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2、Packaging Characteristics

Table 2 Package Characteristics

Symbol	Description	Value	Unit	Note
R _{HS}	High-side Resistance	3.5	mΩ	
R _{LS}	Low-side Resistance	3.5		
L _S	Stray inductance	12	nH	
V _{ISO}	Isolation Test Voltage RMS, f=50Hz, t=1min	5.0	kV	
Distance	Terminal to Baseplate Creepage Distance	40.0	mm	
	Terminal to Terminal Creepage Distance	30.0	mm	
	Terminal to Baseplate Clearance	30.0	mm	
	Terminal to Terminal Clearance	9.0	mm	
R _{th}	Average Thermal Resistance of Per Upper Switch	0.13	°C/W	
	Average Thermal Resistance of Per Lower Switch	0.13	°C/W	
T _{jmax}	Maximum Junction Temperature	175	°C	
T _{jop}	Operation Junction Temperature	-40 to +150	°C	
T _{STG}	Storage Temperature Range	-40 to +150	°C	
W	Weight	300	g	
Ms	Maximum Mounting Torque	5.0	N·m	

¹ Not subject to production test. Parameter verified by design/characterization.

3、Electrical characteristics

3.1 Characteristics

Table 4 SiC MOSFET characteristics (Tc = 25°C unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
$V_{(BR)DSS}$	Drain-source breakdown voltage	1200	-	-	V	$V_{GS} = 0V, I_D = 100\mu A$	
$V_{GS(th)}$	Gate threshold voltage	2.3	2.8	4.0	V	$V_{DS} = V_{GS}, I_D = 112mA$	
		-	2.0	-	V	$V_{DS} = V_{GS}, I_D = 112mA,$ $T_J = 175^\circ C$	
I_{DSS}	Zero gate voltage drain current	-	4	40	μA	$V_{DS} = 1200V, V_{GS} = 0V$	
I_{GSS}	Gate source leakage current	-	-	400	nA	$V_{GS} = 18V, V_{DS} = 0V$	
$R_{DS(on)}$	Current drain-source on-state resistance	-	3.5	4.5	m Ω	$V_{GS} = 18V, I_D = 400A$	
		-	5.3	-		$V_{GS} = 18V, I_D = 400A,$ $T_J = 175^\circ C$	
gfs	Transconductance	-	212	-	S	$V_{DS} = 20V, I_D = 400A$	
		-	206	-		$V_{DS} = 20V, I_D = 400A,$ $T_J = 175^\circ C$	
$R_{g,int}$	Internal gate resistance	-	1.5	-	Ω	$V_{AC} = 25mV, f = 1MHz,$ open drain	
C_{iss}	Input capacitance	-	19.8	-	nF	$V_{DS} = 1000V, V_{GS} = 0V$	
C_{oss}	Output capacitance	-	1.0	-		$T_J = 25^\circ C, V_{AC} = 25mV$	
C_{rss}	Reverse capacitance	-	0.1	-		$f = 100KHz$	
Q_{gs}	Gate source charge	-	231	-	nC	$V_{DS} = 800V, eV_{GS} = -4/+18V$ $I_D = 400A$	
Q_{gd}	Gate drain charge	-	484	-			
Q_g	Gate charge	-	947	-			
E_{on}	Turn on switching energy	-	17.2	-	mJ	$V_{DS} = 800V, V_{GS} = -4/+18V$ $I_D = 400A, R_g = 2.5\Omega,$ $L = 120\mu H$	
E_{off}	Turn off switching energy	-	15.3	-			

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Table 5 SiC SBD characteristics (Tc = 25°C unless otherwise specified)

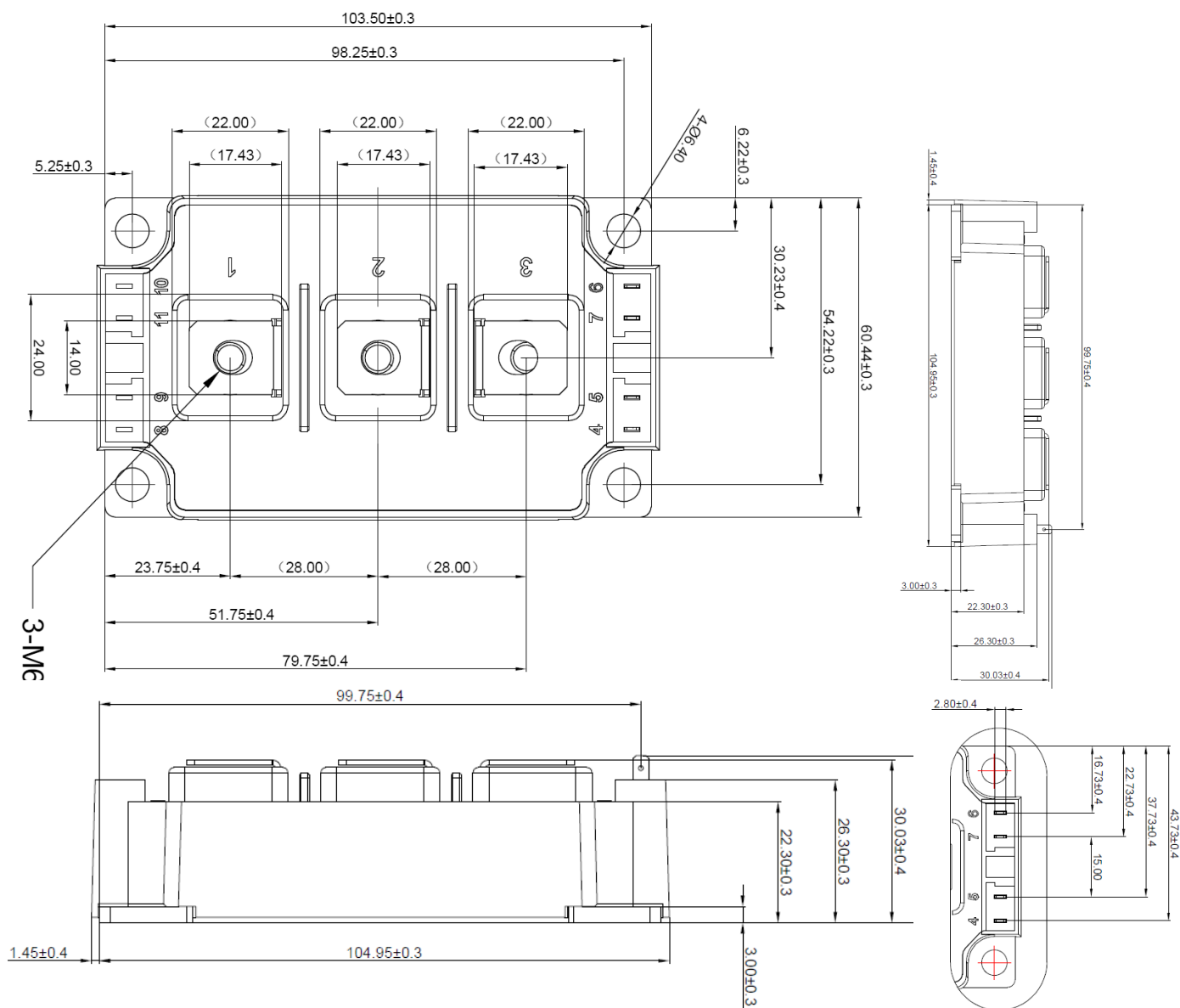
Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
V _{RRM}	Repetitive Peak Reverse Voltage	1200	-	-	V		
I _F	Continuous Forward Current	-	528	-	A	Tc = 25°C	
		-	258	-		Tc = 135 °C	
		-	180	-		Tc = 155 °C	
I _{FSM}	Non-Repetitive Peak Forward Surge Current	-	1040	-	A	T _p =10ms, Half Sine Pulse	
V _F	Forward Voltage	-	1.4	1.8	V	I _{DS} =200A	
		-	1.9	2.5		I _{DS} =40A, T _j =175°C	
I _R	Reverse Current	-	6	600	μA	V _R =1200V	
			60	500		V _R =1200V, T _j =175°C	
Q _c	Total Capacitive Charge	-	628	-	nC	V _R =800V, I _F =40A di/dt=200A/μs	
C	Total Capacitance	-	12.8	-	nF	V _R =0V, f=1MHz	
		-	0.56	-		V _R =400V, f=1MHz	
		-	0,4	-		V _R =800V, f=1MHz	

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4、Package drawing



5、Test conditions

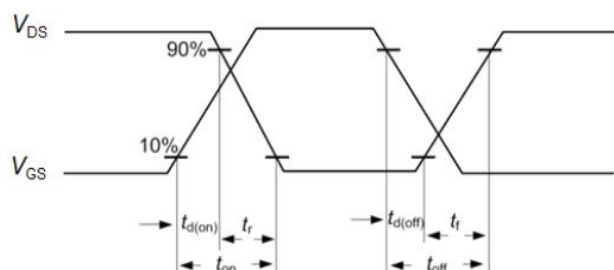


Figure A. Definition of switching times

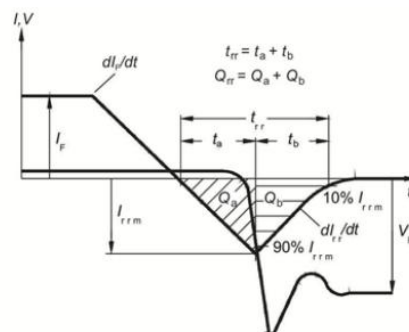


Figure B. Definition of body diode switching characteristics

Revision history

Document version	Date of release	Description of changes	
V01_00	2025-01-04	——	

Attention

1. RoHS compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/ EC (RoHS2), as implemented January 2, 2013.

2. REACH compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Sichain representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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